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DATE: Wednesday, February 09, 2005

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<input type="checkbox"/>	L9	L8 same (power near2 (conserv\$7 or consum\$7))	6
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<input type="checkbox"/>	L4	((turn\$4 adj off) or (shut\$4 adj off) or (shut\$4 adj down) or (power\$4 near2 cutoff)) near2 (bridge or controller)	7177
<input type="checkbox"/>	L3	((turn\$4 adj off) or (shut\$4 adj off) or (shut\$4 adj down) or (power\$4 near2 cutoff)) with (bridge or controller)	22365
<input type="checkbox"/>	L2	((turn\$4 adj off) or (shut\$4 adj off) or (shut\$4 adj down) or (power\$4 near2 cutoff)) same (bridge or controller)	40571
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Mar 16, 2004

TITLE: System and method for operating a system with redundant peripheral bus controllers

The enable lines 65 and 63 connect also to the power controller 62. As the front panel board 66 is removed, the power controller turns off and powers down the board. The power ramps down, however, so that the switches 60 turn off before the power is fully off. This ensures that the switches 60 operate properly to protect the bus signal pins as the board is removed.

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L5: Entry 8 of 15

File: USPT

Sep 23, 1997

DOCUMENT-IDENTIFIER: US 5671368 A

TITLE: PC card controller circuit to detect exchange of PC cards while in suspend mode

Detailed Description Text (20):

In addition to the normal operating mode and the lower power, suspend operating mode of the PC Card controller 16, the computer 10 may be designed to permit a computer program executed by the CPU 12 to turn-off electrical power that is normally supplied to the PC Card controller 16. To prevent failing to detect an exchange of PC Cards 24 while electrical power to the PC Card controller 16 is turned-off, a power-on initialization sequence performed by the PC Card controller 16 every time the PC Card controller 16 is turned-on also causes the fake interrupt-generator 86 to generate an interrupt faking insertion of a PC Card 24 into the PC Card socket 22 regardless of whether such an event has actually occurred. In this way, even if electrical power is removed from the PC Card controller 16, a PC Card controller 16 in accordance with the present invention prevents the computer 10, upon restoring the PC Card controller 16 to normal operation after being turned-off, from applying an improper voltage to a PC Card 24 that was exchanged for another, different voltage, PC Card 24 while the PC Card controller 16 was turned-off, or from experiencing a software breakdown due to a failure to detect an exchange of PC Cards while the PC Card controller 16 was turned-off.

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☐ 1. Document ID: JP 2001211402 A

L10: Entry 1 of 1

File: JPAB

Aug 3, 2001

PUB-NO: JP02001211402A

DOCUMENT-IDENTIFIER: JP 2001211402 A

TITLE: TELEVISION RECEIVER

PUBN-DATE: August 3, 2001

INVENTOR-INFORMATION:

NAME

COUNTRY

KONDO, TAKAHIRO

ASAO, MOTOAKI

ASSIGNEE-INFORMATION:

NAME

COUNTRY

SANYO ELECTRIC CO LTD

APPL-NO: JP2000015337

APPL-DATE: January 25, 2000

INT-CL (IPC): H04 N 5/63; G06 K 19/00; H04 N 7/167; H04 N 7/20

ABSTRACT:

PROBLEM TO BE SOLVED: To provide a television receiver that can reduce power consumption.

SOLUTION: The television receiver is provided with a switch that receives a digital reception circuit power supply even at power off of the receiver and turns on/off the supply of the digital reception circuit to a digital reception circuit, a means that discriminates whether or not a memory card is connected to a memory card connector at power off of the receiver, a means that turns on the switch when it is discriminated that the memory card is connected to the memory card connector, and a means that turns off the switch when it is discriminated that the memory card is not connected to the memory card connector.

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